Notice of References Cited Application/Control No. | Applicant(s)/Patent Under | Reexamination | HUANG ET AL. | Examiner | Art Unit | Insun Kang | 2193 | Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-2005/0102658	05-2005	Li et al.	717/140
*	В	US-2004/0255279	12-2004	Rawsthorne et al.	717/136
*	С	US-2002/0095667	07-2002	Archambault, Roch Georges	717/154
*	D	US-4,961,141	10-1990	Hopkins et al.	717/151
*	Е	US-6,305,014	10-2001	Roediger et al.	717/154
*	F	US-5,530,866	06-1996	Koblenz et al.	717/144
	G	US-			
	Н	US-		0	
	ı	US-			
	J	US-			
	κ	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	. Name	Classification
	N	·	,			
	0					
	Р					
	Q	33300 0				
	R	·				
	S			· C		
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hirnschrott et al., "Graph Cooring vs. Optimal Register Allocation for Optimizing Compilers," Springer, 10/22/2003
	v	Beatty, Register assignment algorithm for generation of highly optimized object code, IBM, 1974
	w	Scholz et al., "Register Allocation for Irregular Architectures," ACM, 2002
	. x	Huang et al., "Loop Transformations for Architectures with Partitioned Register Banks," ACM, 2001

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination HUANG ET AL. | Examiner | Art Unit | Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-			·
	С	US-			
	D	US-			
	E	US-			
	F	US-		•	
	G	US-			
	Ą	US-			
	1	US-		,	·
	J	US-			
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р		2			
	Q					
	R.					
	S					
	Т					*

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	υ	Kolson et al., "Optimal Register Assignment to Loops for Embedded Code Generation," ACM, 1996
	٧	
	W	
	×	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.